

PAT-NO: JP406052241A  
DOCUMENT-IDENTIFIER: JP 06052241 A  
TITLE: NESTING STRUCTURE MESSAGE RETRIEVING DEVICE  
PUBN-DATE: February 25, 1994

**INVENTOR-INFORMATION:**

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INT-CL (IPC): G06 F 015/417

**ABSTRACT:**

PURPOSE: To constitute the device so that attributes such as an NL(nest level) of a token train and an ID code, etc., stored in a message memory are decoded at every token, and when the token train having plural pieces of attributes is retrieved, it can be executed by a single search.

CONSTITUTION: A message memory 1 for storing a message containing an array of a header part and a body part is provided in the body part by a nesting structure. A retrieval condition memory 2 for holding a retrieval condition containing plural pieces of requests NL/ID is provided. A retrieval condition collating circuit 4 for collating an output of the retrieval condition memory 2 and an output of the NL/ID detected from the message memory 1 is provided. An EA calculating circuit 7 for calculating and generating a read-out address of the message by using an output of the retrieval condition collating circuit 4, an internal address generating circuit 8 and an address generating circuit 3 are provided. By the retrieval condition collating circuit 4, even in the case of plural pieces of retrieval conditions, the body part of a token of mismatching is read-skipped, and the retrieval is executed by a signal retrieval processing.

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CONSTITUTION: A message memory 1 for storing a message containing an array of a header part and a body part is provided in the body part by a nesting structure. A retrieval condition memory 2 for holding a retrieval condition containing plural pieces of requests NL /ID is provided. A retrieval condition collating circuit 4 for collating an output of the retrieval condition memory 2 and an output of the NL /ID detected from the message memory 1 is provided. An EA calculating circuit 7 for calculating and generating a read-out address of the message by using an output of the retrieval condition collating circuit 4, an internal address generating circuit 8 and an address generating circuit 3 are provided. By the retrieval condition collating circuit 4, even in the case of plural pieces of retrieval conditions, the body part of a token of mismatching is read-skipped, and the retrieval is executed by a signal retrieval processing.

**Application Date - APD (1):**

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